

**COURSE OF STUDY** *Physics (LM-17)*
**ACADEMIC YEAR** 2024-2025

**ACADEMIC SUBJECT** *Laboratory of Data Acquisition Technologies*

General information	
Year of the course	2nd
Academic calendar (starting and ending date)	1 <sup>st</sup> semester: September – December 2024
Credits (CFU/ETCS):	6
SSD	FIS/01
Language	ENGLISH
Mode of attendance	Compulsory

Professor/ Lecturer	
Name and Surname	Marilisa De Serio - Saverio Simone
E-mail	marilisa.deserio@uniba.it - saverio.simone@uniba.it
Telephone	+39 080 5443182 - +39 080 5443193
Department and address	Physics Department, via Amendola 173, Bari, office 114 – office 115
Virtual room	<i>Microsoft Teams code:</i> tlxjgfs
Office Hours (and modalities: e.g., by appointment, on line, etc.)	Students are invited to send an e-mail to arrange individual or group meetings.

Work schedule			
Hours			
Total	Lectures	Hands-on (laboratory, workshops, working groups, seminars, field trips)	Out-of-class study hours/ Self-study hours
150	24	45	81
CFU/ETCS			
6	3	3	

<b>Learning Objectives</b>	The course is intended to introduce the basic concepts of data-acquisition systems used in modern physics experiments focussing on the development of high-level software to control external devices/sensors interfaced to the PC.
<b>Course prerequisites</b>	Basic knowledge of electronics. Basic knowledge of computer programming.

<b>Teaching strategies</b>	Lectures (with slides). Laboratory exercises in small groups.
<b>Expected learning outcomes in terms of</b>	
<b>Knowledge and understanding:</b>	<ul style="list-style-type: none"> <li>o Understanding the scientific method, the nature, and the methods of research in Physics</li> <li>o Knowledge of advanced mathematical tools commonly used in basic and applied research fields</li> <li>o Knowledge of the technologies required in experimental physics</li> <li>o Knowledge of advanced instrumentation in experimental physics</li> <li>o Knowledge and understanding of basic concepts of modern digital data-acquisition systems.</li> <li>o Knowledge of most commonly used I/O techniques for computer-based data acquisition.</li> </ul>
<b>Applying knowledge and understanding:</b>	<ul style="list-style-type: none"> <li>o Ability to identify the essential elements of a phenomenon</li> </ul>

	<ul style="list-style-type: none"> <li>o Ability to use analogy to apply known solutions to new problems (problem solving)</li> <li>o Ability to use analytical and numerical mathematical computation tools</li> <li>o Ability to use electronic and computer technologies and their application to experimental data acquisition</li> <li>o Ability to apply most commonly used I/O techniques for computer-based data acquisition.</li> <li>o Ability to develop simple high-level software applications for data-acquisition using computer-controlled electronic devices.</li> </ul>
<p><b>Soft skills</b></p>	<ul style="list-style-type: none"> <li>● <b>Making informed judgments and choices</b> <ul style="list-style-type: none"> <li>o Ability to work with increasing levels of autonomy, including taking responsibility in project planning and managing facilities.</li> <li>o Awareness of safety issues in laboratory activities</li> <li>o Ability to work in a laboratory.</li> <li>o Ability to identify adequate hardware and software solutions for specific problems/applications.</li> </ul> </li> <li>● <b>Communicating knowledge and understanding</b> <ul style="list-style-type: none"> <li>o Competence in communication in Italian and English in advanced fields of Physics</li> <li>o Ability to use adequate technical language.</li> <li>o Teamwork skills.</li> </ul> </li> <li>● <b>Capacities to continue learning</b> <ul style="list-style-type: none"> <li>o Acquisition of basic knowledge tools for continuous learning and knowledge updates</li> <li>o Ability to consult bibliographic/technical material in Italian or English.</li> </ul> </li> </ul>
<p><b>Syllabus</b></p>	
<p><b>Content knowledge</b></p>	<p>Introduction to modern data acquisition systems and applications.</p> <p>Computer architecture: processor, cache memory and main memory, motherboard, buses, I/O devices.</p> <p>Interconnection structures: characteristics of buses (type, width, arbitration, timing, data transfer modes), bus interconnection, multiple bus hierarchies.</p> <p>I/O modules. I/O techniques: programmed I/O, interrupt-driven I/O, Direct Memory Access.</p> <p>Interfacing external devices to the PC with I/O modules. PCI and PCI-X buses, USB, PCI Express bus.</p> <p>Sensors.</p> <p>Analog to digital interface: sampling of analog signals, aliasing and quantization; Sample and Hold; Analog to Digital Conversion (ADC): counter type ADC, successive approximation ADC, flash ADC; Digital to Analog Conversion (DAC): binary-weighted resistor DAC.</p> <p>Readout electronics for signal detection: signal conditioning (amplification, shaping), pedestal subtraction; FPGA-based signal processing: data timestamping, zero-suppression. Trigger.</p> <p>Ethernet-based data acquisition: transmission protocols; client – server architecture; Ethernet-based distributed data acquisition systems.</p> <p><b>Laboratory exercises:</b> Part 1. Introduction to programming.</p>

	<ul style="list-style-type: none"> <li>☐ Fundamentals of C language: <ul style="list-style-type: none"> <li>- Handling binary data, bitwise operators.</li> </ul> </li> <li>☐ Introduction to the ROOT framework for data representation and analysis.</li> </ul> <p>Part 2. Use of data acquisition boards with PCI interface (National Instruments PCI-6503, PCI-62212).</p> <ul style="list-style-type: none"> <li>☐ Temperature monitoring using a sensor connected to an 8-bit ADC.</li> <li>☐ Sampling and reconstruction of a sinusoidal signal.</li> <li>☐ Triggered acquisition of pulsed signals.</li> </ul> <p>Part 3</p> <ul style="list-style-type: none"> <li>☐ Ethernet-based data acquisition, client – server architecture.</li> <li>☐ Detector calibration using FPGA-based readout electronics.</li> </ul>
<b>Texts and readings</b>	<ul style="list-style-type: none"> <li>- W. Stalling, Computer organization and architecture, Pearson Edition (Ch. 3 – 7, Ch. 4 – 5 - 6)</li> <li>- S. Derenzo, Practical Interfacing in the Laboratory, Cambridge Edition (Ch. 1, Ch. 3, Par. 5.8.1)</li> <li>- W. Kernighan and D. Ritchie, The C programming language, Prentice Hall Edition</li> <li>- <a href="http://root.cern.ch/">http://root.cern.ch/</a></li> </ul>
<b>Notes, additional materials</b>	Lecture slides. Additional material on specific topics provided during the course.
<b>Repository</b>	Microsoft Teams – code <b>tlxjgfs</b>

<b>Assessment</b>	
Assessment methods	Written laboratory reports or oral presentations on laboratory projects. Practical exam to assess laboratory skills. Oral exam.
Assessment criteria	<p>The student knows</p> <ul style="list-style-type: none"> <li>o the basic concepts of modern digital data-acquisition systems;</li> <li>o the most commonly used I/O techniques for computer-controlled data acquisition;</li> <li>o how to apply I/O techniques;</li> <li>o how to implement I/O techniques and develop simple software applications to interface external devices/sensors to the PC;</li> <li>o how to write a laboratory report.</li> </ul> <p>The student is able to consult technical documentation and communicates effectively using adequate technical language.</p>
Final exam and grading criteria	Laboratory reports (10%). Practical exam (40%). Oral exam (50%). The final grade is expressed on a 30-point scale. The minimum passing grade is 18/30, the maximum grade is 30/30 cum laude.
<b>Further information</b>	.